

IN THE CLAIMS:

Please amend claims 1-13, as shown in the complete list of claims that is presented below.

1. (currently amended) A method of write-protecting a MAC address of a peripheral ~~terminal~~ device, wherein the MAC address is stored in a first memory (CMOS memory)[[.]], and a backup MAC address is stored in a second memory (DMI Flash Memory), the method comprising:

disabling programs capable of erasing the MAC address stored in the first memory;
executing a DMI setting to write-protect the MAC address stored in the second memory; and
providing a program capable of pre-storing [[the]] an original MAC address for restoring.

2. (currently amended) The method of claim 1 further comprising:

~~backing-up~~ restoring the MAC address stored in the first memory using the MAC address stored in the second memory when the MAC address stored in the first memory is incorrect.

3. (currently amended) The method of claim 1 further comprising:

~~backing-up~~ restoring the MAC address stored in the second memory using the MAC address stored in the first memory when the MAC address stored in the second memory is incorrect.

4. (currently amended) The method of claim 1, wherein the peripheral ~~terminal~~ device is a local area network (LAN).

5. (currently amended) The method of claim 1, wherein the peripheral ~~terminal~~ device is an IEEE1394 device.

6. (currently amended) The method of claim 1, further comprising:

checking whether an identification code of a MAC address stored in the second memory is correct;
checking whether the MAC address stored in the first memory is correct if the MAC address stored in the second memory is correct;
copying the MAC address stored in the second memory to a ~~determined~~ predetermined register if the MAC address stored in the first memory is incorrect; and
~~hiding a~~ disabling the function of setting the MAC address.

7. (currently amended) The method of claim 6, wherein the step of checking the identification code further comprising, comprises: when the identification code of the MAC address stored in the second memory is erroneous;

determining whether the identification code of the MAC address in the second memory has been updated when the identification code of the MAC address stored in the second memory is erroneous;
setting ~~[[the]]~~ an updated flag of the MAC address of the second memory if the identification code of the MAC address of the second memory has been updated;
copying the MAC address stored in the second memory in a ~~determined~~ predetermined register; and
~~hiding~~ disabling the setting function of the MAC address.

8. (currently amended) The method of claim ~~[[6]]~~ 7, wherein the step of determining the code identification further comprising, comprises: if the identification code (checksum) of the MAC address in the second memory has not been updated;

determining whether the identification code stored in the first memory is correct if the identification code (checksum) of the MAC address in the second memory has not been updated;
copying the MAC address in the first memory to the second memory if the identification code in the first memory is correct; and
setting the updated flag of the MAC address in the second memory.

9. (currently amended) ~~[[A]]~~ The method of claim 1, wherein the second memory is a non-volatile memory.

10. (currently amended) A method of updating a BIOS setting of a motherboard, the motherboard comprising a first memory (CMOS memory) for storing a media access control (MAC) address of a peripheral terminal device, and a second memory (DMI Flash Memory) for backing up the MAC address of the peripheral terminal device, the method comprising:
updating the MAC address stored in the first memory using the MAC address stored in the second memory, wherein the MAC address stored in the first memory is set as read-only when a utility program for updating a DMI setting is executed;
and
updating the BIOS setting of the second memory using the BIOS setting stored in the first memory.

11. (currently amended) The method of claim 10 further comprising ~~backing up~~ restoring the MAC address stored in the first memory using the MAC address stored in the second memory when the MAC address stored in the first memory is incorrect.

12. (currently amended) The method of claim 10 further comprising ~~backing up~~ restoring the MAC address stored in the second memory using the MAC address stored in the first memory when the MAC address stored in the second memory is incorrect.

13. (currently amended) The method of claim 10, wherein the peripheral ~~terminal~~ device is a peripheral device following an IEEE1394 specification.

14. (original) The method of claim 10, wherein the second memory is a non-volatile memory.